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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Makoto KUDO et al.

Group Art Unit: 2114

Application No.: 09/424,667

Filed: March 15, 2000

Docket No.: 104822

For: MICROCOMPUTER, ELECTRONIC EQUIPMENT AND DEBUGGING SYSTEM

REPLACEMENT OF FORM PTO-1449 FILED APRIL 21, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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Technology Center 2100

Further to the Information Disclosure Statement with Form PTO-1449 filed April 21, 2005, enclosed is a Replacement Form PTO-1449 to correct a typographical error. Specifically, the Form PTO-1449 is amended to properly indicate the pages of the disclosed Yoshida reference. Therefore, "pp. 204-247" is replaced with --pp. 240-247--.

Respectfully submitted,

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JAO:HNM/ale

Date: May 19, 2005

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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 104822		APPLICATION NO. 09/424,667	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Makoto KUDO et al.			
				FILING DATE March 15, 2000		GROUP 2114	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Isao Yoshida, "Function and Application of Serial Port", Transistor Gijutsu, Vol. 32, No. 10, October 1, 1995, pp. 240-247 (w/partial English language translation of article)					
EXAMINER				DATE CONSIDERED			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: May 19, 2005